



Introduction

CAN FD (CAN with Flexible Data-Rate) is a newer extension version of the CAN 2.0 protocol. It was developed by Bosch and was released in 2012. It has been significantly improved during the standardization process and is nowadays in ISO 11898-1:2015. The CAN FD speeds up the data transmission and packs more data into each message.

PISO-CAN400U-FD series board is a very powerful and economic solution for an active CAN board, containing four CAN channels that cover a wide range of CAN applications. It uses Microchip CAN FD controllers and TI TCAN1042HG series transceivers, which provide bus arbitration and error detection features, combined with auto-correction and re-transmission functionality. As the PISO-CANFD series board is state-of-the-art, it can be installed in either a Universal PCI bus.

PISO-CAN400U-FD-D	PISO-CAN400U-FD-T		
Universal PCI, 3.3 V and 5 V, 33 MHz, 32-bit, plug and play			
By DIP	switch		
Microchip M	1CP2518FD		
TI TCAN1042HG			
4			
9-pin Male D-Sub	5-pin screw terminal block		
CAN bit rates: 10 ~ 1000 kbps, CAN FD bit rates for data field: 100 ~ 10000 kbps			
3000 VDC for DC-to-DC, 3000 Vrms for photo-couple			
Jumper for 120 Ω terminator resistor			
200 mA	A @ 5 V		
Windows 7/8.1/1	.0 (32-bit/64-bit)		
C#.Net, VB.N	et, VC++.Net		
121.7 x 21.6 x 9	92.7 (W x L x H)		
0 to 6	50 °C		
-20 to	70 ℃		
5 to 85% RH, N	Ion-condensing		
	PISO-CAN400U-FD-D Universal PCI, 3.3 V and and By DIP Microchip N TI TCAN 9-pin Male D-Sub CAN bit rates: 1 CAN bit rates: 1 CAN bit rates for data 3000 VDC for DC-to-DC, 30 Jumper for 120 Ω 200 mA 200 mA 121.7 x 21.6 x 50 0 to 0 -20 to 5 to 85% RH, N		

Specifications

Attention:

The maximum CAN FD data rate can be exceeded depending on the concrete operating conditions (cable length, network topology, settings,...), but it can also not be reached.

🖿 Utility

h PISO CANFI	D Utility v1.0	0.0				
File Connect	View Help					
Port 1	Port 2	Poet 3	Port 4			
CAN1 S	end Messa	age				
Send M	essage Co	nfiguration				
Mode	ID (He	x) T	ype	DL Data	0000	Timer (ms)
11-64 ID	M 000	1:ÇA	N M	8 00000000	0000	
No MOD	E ID(hex)) Type	DL	Data	Timer	Status
Add	Modify D	Velete De	il Table	Send Cir Crt	SendCmt	
CAN1 R	eceive Me	ssage	0.0-	and Martin Companying Martin		Secolling
No	MODE 1	Diberi T	Uno Di	Data		TreeStampin 6
		Dinexy 1	Je u	Cons.		
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<u> </u>		Start	Decreed	Pr Pause Clear	RecyCet	
		- Otart P	TREE TO T	PA Pause Ciear	neeren	
Model: P	ISO-CAN400	U-FD		PortStatus: Enable	BR: 500.000/20	000.000 kbps 🛒

Hardware Architecture



Pin Assignments

Pin Assignments for the 5-pin screw terminal connector			
Pin No.	Name	Description	
1	CAN_GND	CAN_Gnd, signal line for the CAN port.	5-pin screw terminal block
2	CAN_L	CAN_Low, signal line for the CAN port.	
3	F.G.	Frame Ground.	3 S. C. C. NIA
4	CAN_H	CAN_High, signal line for the CAN port.	Can and
5	N/A	Not used	

Applications



Pin Assignments for the 9-pin Male D-Sub connector			
Pin No.	Name	Description	
1	N/A	Not used	
2	CAN_L	CAN_Low, signal line for the CAN port.	
3	CAN_GND	CAN_Gnd, signal line for the CAN port.	9-pin D-Sub male connector
4	N/A	Not used	00 CAN_GND
5	N/A	Not used	CAN_H
6	CAN_GND	CAN_Gnd, signal line for the CAN port.	CAN_L
7	CAN_H	CAN_High, signal line for the CAN port.	
8	N/A	Not used	
9	N/A	Not used	

Software Architecture

9	Software Architecture	9
	User Program	
	CAN FD Library (User Mode)	
	CAN FD Library (Kemel Mode)	
	Hardware Platform	

Dimensions (Units: mm)

PISO-CAN400U-FD-D:



Ordering Information

PISO-CAN400U-FD-D CR	4-port Isolated Protection CAN FD Universal PCI Card with 9-pin D-sub connector (RoHS)
PISO-CAN400U-FD-T CR	4-port Isolated Protection CAN FD Universal PCI Card with 5-pin screw terminal connector (RoHS)